

## Set 4 Expanded Mark Scheme – PLCs

Q	Expanded Answer Guidance	Marks
1a	Programmable Logic Controller (accept minor wording variations)	1
1b	Other PLC types: <ul style="list-style-type: none"> <li>• Modular PLC</li> <li>• Rack-mounted PLC</li> </ul> Accept equivalents	2
1c	Unitary PLC: <ul style="list-style-type: none"> <li>• All components in one unit</li> <li>• Fixed I/O</li> <li>• Compact and low cost</li> </ul> Compared to others: <ul style="list-style-type: none"> <li>• Modular/rack systems allow expansion</li> <li>• More flexible I/O configuration</li> <li>• Easier maintenance and upgrades</li> </ul> Accept: <ul style="list-style-type: none"> <li>• Clear comparison</li> </ul>	6
1d	Advantages of modular/rack PLCs: <ul style="list-style-type: none"> <li>• Expandable I/O</li> <li>• Flexible configuration</li> <li>• Easier fault replacement</li> <li>• Suitable for large systems</li> </ul> Accept: <ul style="list-style-type: none"> <li>• Any valid industrial advantage</li> </ul>	4
2a	Any 5: <ul style="list-style-type: none"> <li>• CPU</li> <li>• Power supply</li> <li>• Input module</li> <li>• Output module</li> <li>• Memory</li> <li>• Communication interface</li> </ul> Accept equivalents	2

2b	<p>CPU:</p> <ul style="list-style-type: none"> <li>• Executes program</li> <li>• Processes inputs</li> <li>• Controls outputs</li> <li>• Performs logic operations</li> </ul> <p>Accept:</p> <ul style="list-style-type: none"> <li>• Description of control role</li> </ul>	3
2c	<ul style="list-style-type: none"> <li>• RAM</li> <li>• ROM / Flash / EEPROM</li> </ul> <p>Accept equivalents</p>	2
2d	<p>RAM:</p> <ul style="list-style-type: none"> <li>• Volatile</li> <li>• Temporary storage</li> </ul> <p>ROM/Flash:</p> <ul style="list-style-type: none"> <li>• Non-volatile</li> <li>• Stores program permanently</li> </ul> <p>Accept:</p> <ul style="list-style-type: none"> <li>• Clear comparison</li> </ul>	4
3a	<p>Either:</p> <ul style="list-style-type: none"> <li>• Normally open contact (NO)</li> <li>• Input contact</li> </ul>	1
3b	<ul style="list-style-type: none"> <li>• Address (e.g. I0.0)</li> <li>• Identifies input/output location</li> </ul> <p>Accept:</p> <ul style="list-style-type: none"> <li>• Tag/address explanation</li> </ul>	2

3c	<p>Band 1 (1–2):</p> <ul style="list-style-type: none"><li>• Basic description</li></ul> <p>Band 2 (3–4):</p> <ul style="list-style-type: none"><li>• Explains logic flow</li></ul> <p>Band 3 (5–6):</p> <ul style="list-style-type: none"><li>• Full explanation of operation</li><li>• Includes example application (e.g. motor control, conveyor)</li></ul> <p>Accept:</p> <ul style="list-style-type: none"><li>• Clear sequence of operation</li></ul>	6
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